A Semi-Implicit numerical method for Discontinuous Hermite Collocation on GPUs

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# Talk Overview

- Development of a parallel algorithm for the Discontinuous Hermite Collocation method on Parallel Architectures with accelerators
- Parallel implementation on computing architectures with GPUs





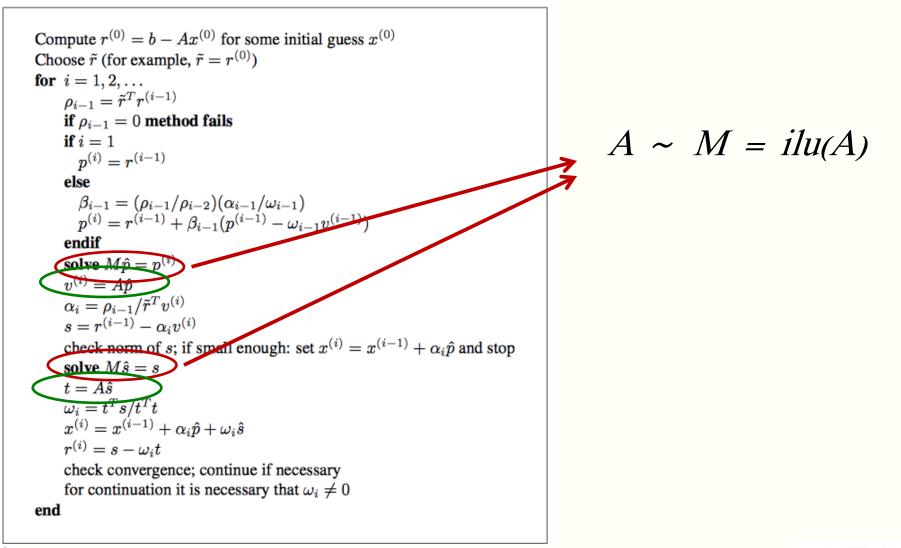
### Semi-Implicit Discontinuous Hermite Collocation Method

NPUT 
$$a_{old}$$
,  $B$ ,  $A_0$ ,  $A$ ,  $A_b$   
for  $t = dt$  to  $t_{max}$  with step  $dt$  do  
compute  $a_0 = A_0 a_{old}$   
if  $t \le 2 dt$  then  
solve  $A_b a_{new} = a_{old}$  with BiCGSTAB  
else  
solve  $A a_1 = a_0$  with BiCGSTAB  
compute  $a_0 = -dt \frac{\sqrt{3}}{3} B a_1$   
solve  $A a_2 = a_0$  with BiCGSTAB  
compute  $a_2 = \frac{dt}{2} B (a_1 + a_2)$   
solve  $A_0 a_{new} = a_2$  with BiCGSTAB  
endif  
compute  $a_{old} = a_{new}$   
endif





### Preconditioned BiCGSTAB Wethod ...

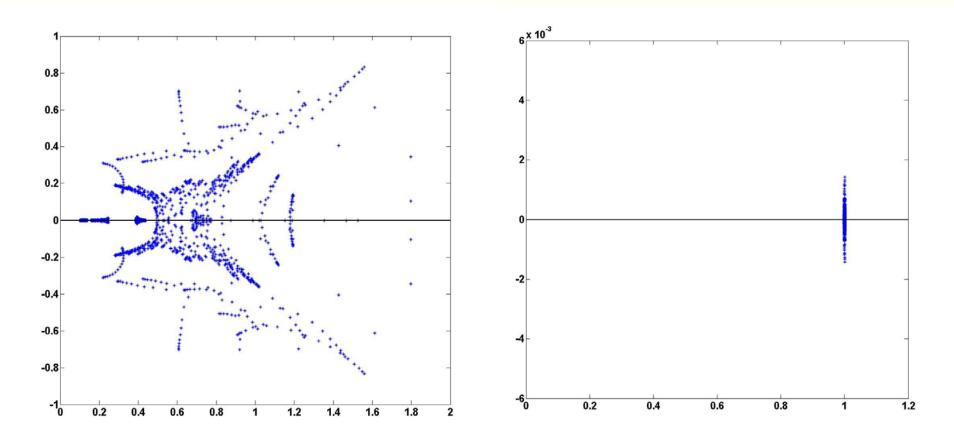






### Eigenvalue Distribution ...

Matrix  $A_b$ 

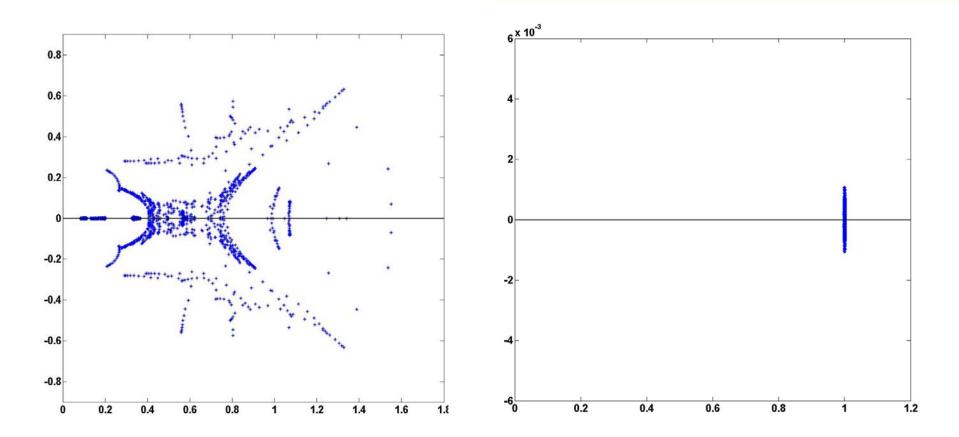






### Eigenvalue Distribution ...

Matrix A

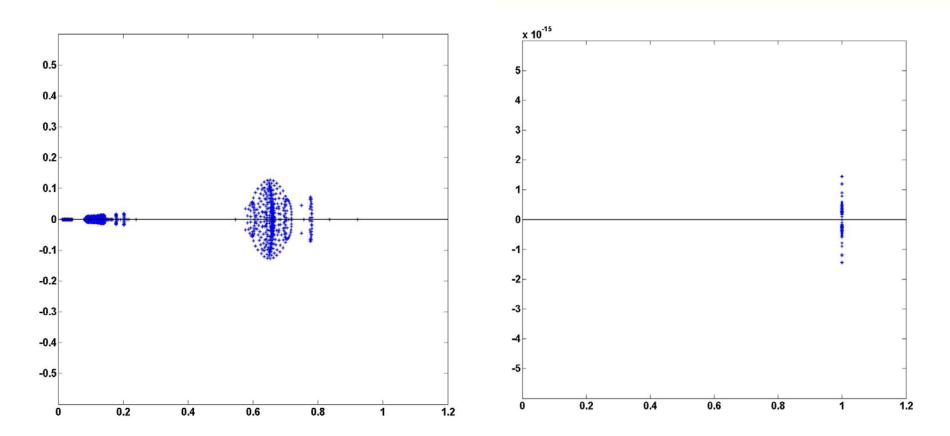






### Eigenvalue Distribution ...

Matrix  $A_o$ 







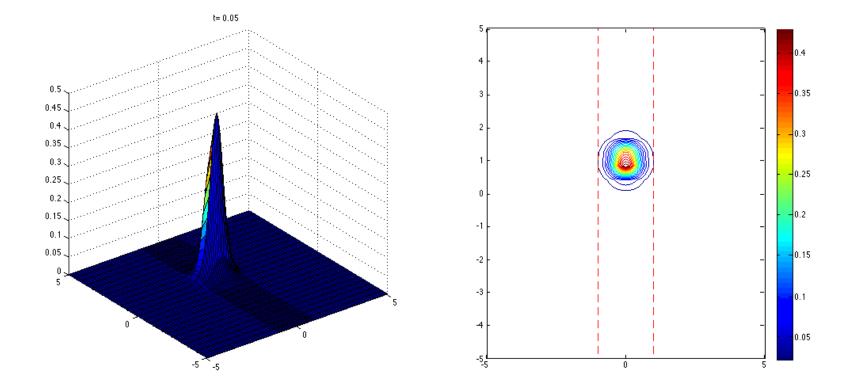
### Parallel BicGSTAB

- All basic linear algebra operations are performed on the GPU
- The preconditioning procedure Mz = t with M=LU is performed on the CPU
  - Step 1:GPU sends to CPU vector tStep 2:CPU solution of Ly = tStep 3:CPU solution of Uz = yStep 4:CPU sends to GPU vector z





### The test problem ...







# HP SL390s - Tesla M2070 GPUs

#### HP SL390s





6 core Xeon@2.8GHz 24GB memory Oracle Linux 6.3 x64 PGI 14.5 Cuda Fortran Cuda toolkit 6.0 PCI-e gen2 x16 **The Portland Group** 

#### TECHNICAL SPECIFICATIONS

	Tesla M2070 / M2075
Peak double precision floating point performance	515 Gigaflops
Peak single precision floating point performance	1030 Gigaflops
CUDA cores	448
Memory size (GDDR5)	6 GigaBytes
Memory bandwidth (ECC off)	150 GBytes/sec







TECHNICAL UNIVERSITY OF CRETE APPLIED MATHEMATICS AND COMPUTERS LABORA

NVIDIA<sup>®</sup> CUDA<sup>™</sup>

Parallel Programming and Computing Platform

- MatLab R2012b
- PGI 14.5 Cuda Fortran
   Cuda toolkit 6.0 cuBLAS cuSRARSE
   for GPU operations
   SparseKit for CPU operations





```
module bicgstabGPU
   contains
    subroutine bicgstabGPU(n,x,b,A,L,U,iA,iL,jA,jL,jU,
                        nzA,descrA,handle,error,r,rh,pi,ph,
+
                        t,s,sh,ui,istep,temp,ttt)
+
 implicit real*8 (a-h,o-z)
 real*8 ttt(n),L(*),U(*),temp(n)
 integer*8 handle,descrA,iA,jA,A,
            x,b,r,rh,pi,ph,s,sh,ui,t
+
 integer cusparse_dcsrmv,iL(n+1),jL(*),jU(*),
    cuda_memcpy_c2fort_real,cuda_memcpy_fort2c_real
+
 imaxstep=istep
 tol=error
 istep=0
 dnrmb=cublas_dnrm2(n,b,1)
```

```
call cublas_dcopy(n,b,1,x,1)
  istatus=cusparse_dcsrmv(handle,0,n,n,nzA,1.0d0,descrA,A,iA,jA,
+ x,0.0d0,r)
```





```
call cublas_dscal(n,-1.0d0,r,1)
      call cublas_daxpy(n,1.0d0,b,1,r,1)
      call cublas_dcopy(n,r,1,rh,1)
999
      continue
      istep=istep+1
      if (istep.gt.1) roip2=roip1
       roip1=cublas_ddot(n,rh,1,r,1)
      if (istep.eq.1) then
       call cublas_dcopy(n,r,1,pi,1)
      else
       bi=(roip1/roip2)*(ai/wi)
       call cublas_daxpy(n,-wi,ui,1,pi,1)
       call cublas_dcopy(n,r,1,t,1)
       call cublas_daxpy(n,bi,pi,1,t,1)
       call cublas_dcopy(n,t,1,pi,1)
      endif
```





```
icudaStat = cuda_memcpy_c2fort_real(ttt,pi,n*8,2)
call lsol(n,temp,ttt,L,jL,iL)
call udsol(n,ttt,temp,U,jU)
icudaStat3 = cuda_memcpy_fort2c_real(ph,ttt,n*8,1)
```

```
icudaStat = cuda_memcpy_c2fort_real(ttt,s,n*8,2)
call lsol(n,temp,ttt,L,jL,iL)
call udsol(n,ttt,temp,U,jU)
icudaStat3 = cuda_memcpy_fort2c_real(sh,ttt,n*8,1)
```



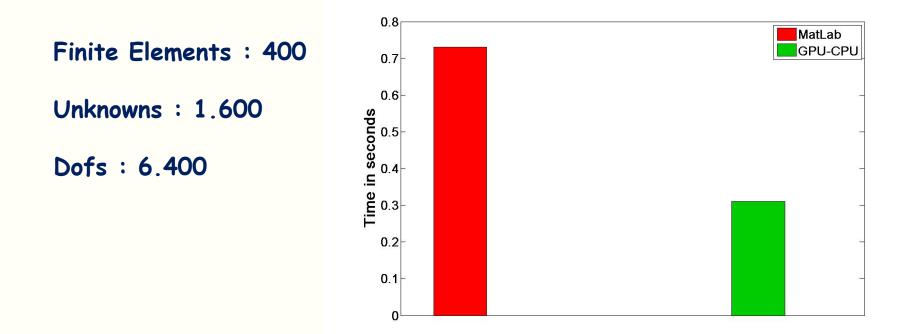


```
istatus=cusparse_dcsrmv(handle,0,n,n,nzA,1.0d0,descrA,A,iA,jA,
          sh,0.0d0,t)
wi=cublas_ddot(n,t,1,s,1)/cublas_ddot(n,t,1,t,1)
 call cublas_daxpy(n,ai,ph,1,x,1)
 call cublas_daxpy(n,wi,sh,1,x,1)
 call cublas_daxpy(n,-wi,t,1,s,1)
 call cublas_dcopy(n,s,1,r,1)
 dnrmr=cublas_dnrm2(n,s,1)
 error=dnrmr/dnrmb
  print*,error,istep
if (wi.ne.0.0d0.and.error.gt.tol.and.istep.lt.imaxstep)
+ goto 999
endif
return
end
     module
end
```



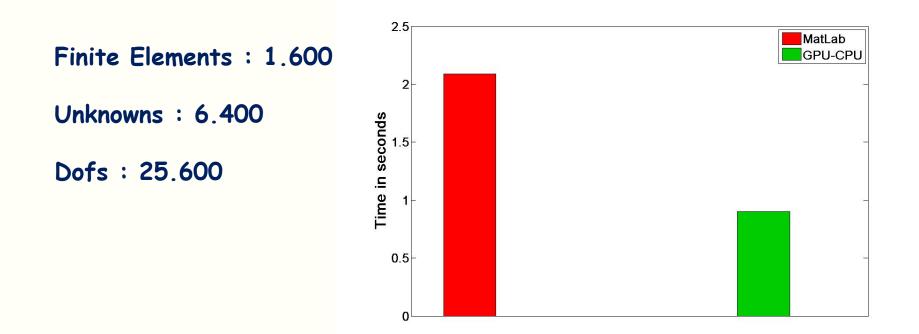
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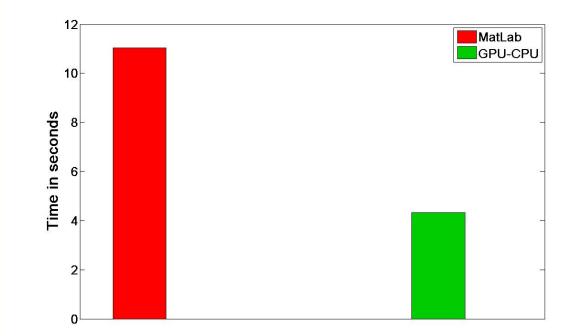








Finite Elements : 6.400 Unknowns : 25.600 Dofs : 102.400



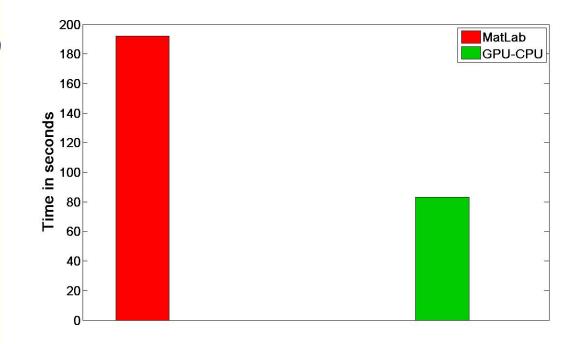




Finite Elements : 25.600

**Unknowns : 102.400** 

Dofs: 409.600





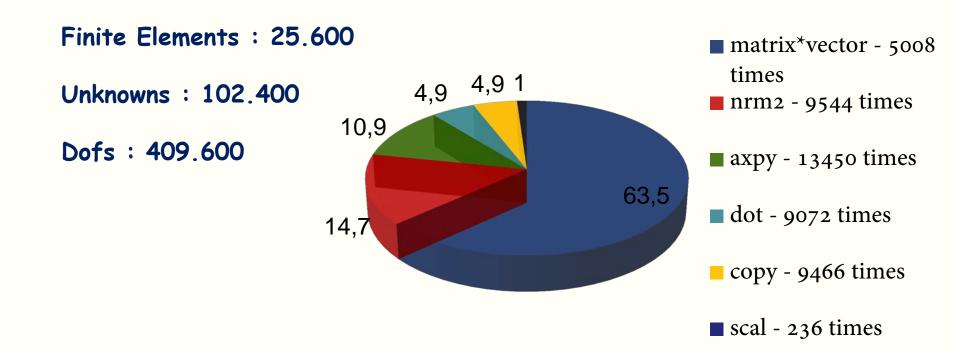


#### Finite Elements : 25.600 Unknowns : 102.400 Dofs : 409.600

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Process "thalis" (31136)							
Thread 4112791520					Session	183.333 s (183,333,456,481 ns)	
L Runtime API					Kernel	2.197 s (2,196,899,375 ns)	
L Driver API					Invocations	5008	
Profiling Overhead					Importance	63.4%	
[0] Tesla M2070							
Context 1 (CUDA)							
└ 🍸 MemCpy (HtoD)							
L T MemCpy (DtoH)							
Compute							
► 🍸 63.4% void csrMv_ci							
└ 🍸 14.6% void nrm2_ker							
└ 🍸 10.9% void axpy_ker							
└─ 🍸 5.0% void dot_kerne							
└ 🍸 4.9% void copy_kern							
└ 🍸 0.7% void reduce_18							
└ 🍸 0.2% memset (0)							
└─ 🍸 0.1% void scal_kerne							
Streams							
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Analysis 🕴 🗔 Details 💂 Console 🗔 Settings							











# Conclusions

- A new parallel algorithm implementing the Discontinuous Hermite Collocation method has been developed.
- The algorithm is realized on machines with GPU accelerators .
- A performance acceleration of up to 50% is observed for fine discretizations over MatLab multithread implementations.





# Future work

 Design an efficient parallel algorithm for machines with multiple GPUs using cuBLAS-XT.



